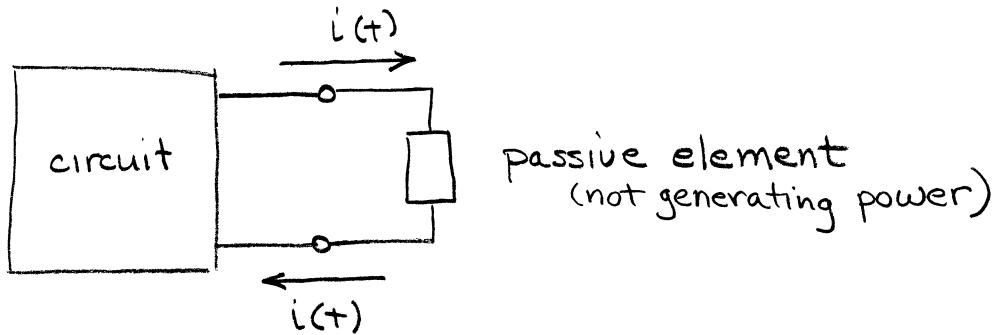
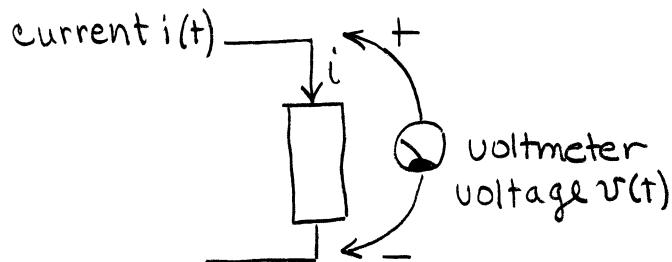


The passive sign convention



The electrons flow through the device being neither created or destroyed.

However, depending upon the device the electrons can lose energy. This is measured by the voltage across the device.

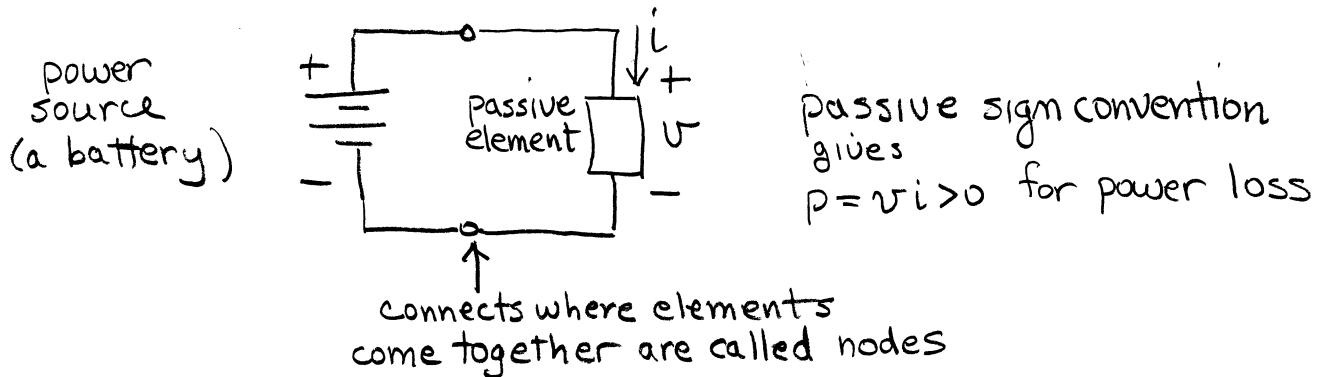


These are reference signs and directions.

When electrons lose energy it is typically manifested as heat. Other possibilities are light, sound and electromagnetic radiation.

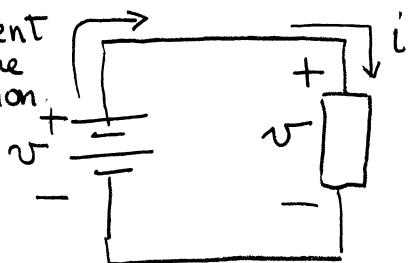
The power lost is $p = u(t) i(t) > 0$

If this device were transferring energy to the electrons this would be indicated by a change in the sign of the voltage, i.e. $p < 0$ for power generation



A battery is an active element (an energy source)

- ② This current must be in the same direction.



①

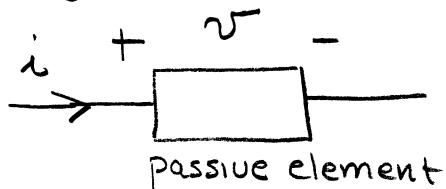
Assume passive element is converting electrical energy to something else.

- ③ by passive sign convention the power loss by the passive element is $p = vi > 0$

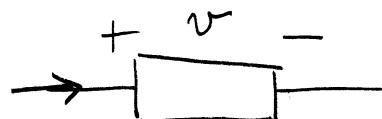
- ④ by passive sign convention the power supplied by the battery is $p = vi < 0$

Note that the voltage across the battery and the passive element are the same.

Passive sign convention



The are the directions and polarities we assume for positive. We can get positive and negative quantities



A current of -5 amperes means the current is actually going in the opposite direction,

GROUND

A° B° C°



symbol for electrical ground
common voltage reference

The notation $v_A(t)$, $v_B(t)$, $v_c(t)$ means the voltage at that node with respect to ground, assumed to be zero.

Voltage ALWAYS is measured between two points.

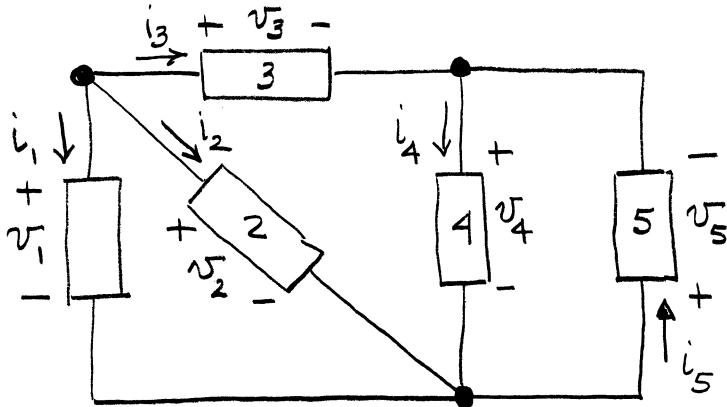
I might also be interested in the voltage

$v_{AB}(t)$ This is the voltage between two nodes. Polarity should be indicated on circuit diagram.

+ v_{AB} -
A° B°



Example 1-3



In this circuit the sign conventions are given on the drawing.
If they are not given you should assign them!

The above labeling follows the passive sign convention.

The following circuit variables are measured.

Complete the missing variables. State whether the device is absorbing or supplying power.

	Device 1	Device 2	Device 3	Device 4	Device 5
v	+100 V		+25V	+75V	-75V
i		+5mA	+5mA		+5mA
P	-1W	+0.5W		0.75W	

$$\text{use } P = vI$$

$$i_1 = \frac{P_1}{v_1} = \frac{-1}{100} = -0.01 \text{ Amp} \quad P_1 < 0 \Rightarrow \text{supplying power}$$

$$v_2 = \frac{P_2}{i_2} = \frac{0.5}{0.005} = 100V \quad P > 0 \Rightarrow \text{absorbing power}$$

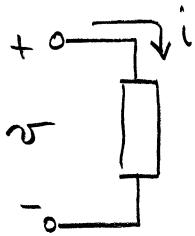
$$P_3 = v_3 i_3 = (25)(0.005) = 0.125 \quad P > 0 \Rightarrow \text{absorbing power}$$

$$i_4 = \frac{P_4}{v_4} = \frac{0.75}{75} = 0.01 \quad P > 0 \Rightarrow \text{absorbing power}$$

$$P_5 = v_5 i_5 = (-75)(0.005) = -0.375 \quad P < 0 \Rightarrow \text{supplying power}$$

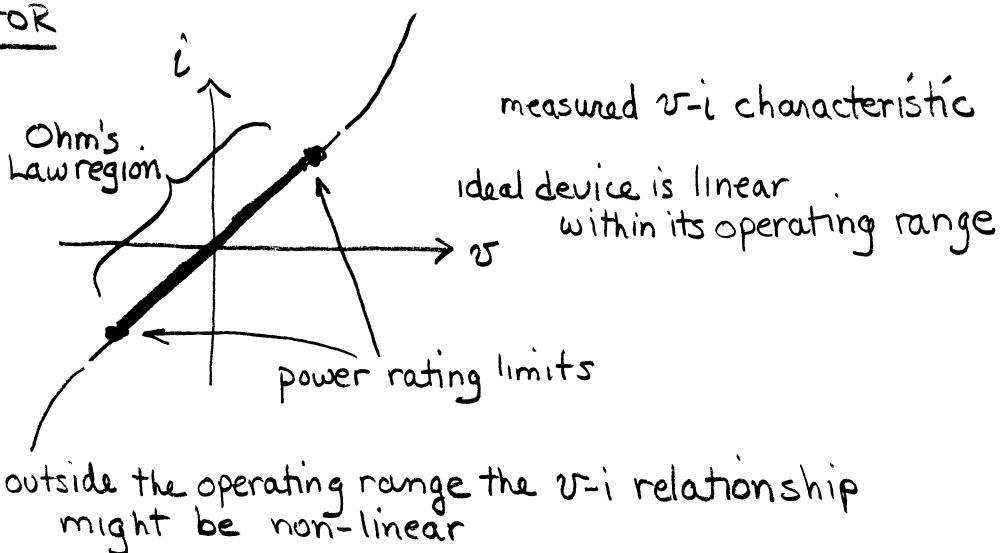
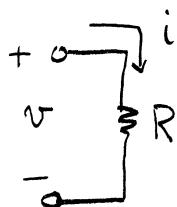
2-1 Element Constraints

A two terminal device is described by its v - i characteristics.



device - the "real" electrical device
element - our model

LINEAR RESISTOR



characteristics are symmetric about origin

For the sign convention given (passive sign convention)

$$i = \left(\frac{1}{R}\right) v \quad \text{where } \frac{1}{R} \text{ is the slope}$$

Normally we write this as

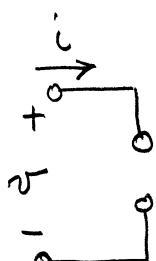
$$\left. \begin{array}{l} v = iR \\ \text{or} \\ i = GV \end{array} \right\} \begin{array}{l} \text{resistance (ohms, } \Omega \text{)} \\ R = \frac{1}{G} \end{array} \begin{array}{l} \text{conductance} \\ (\text{Siemens, S}) \end{array}$$

and call this Ohm's Law.

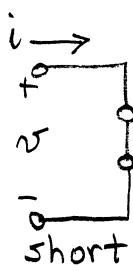
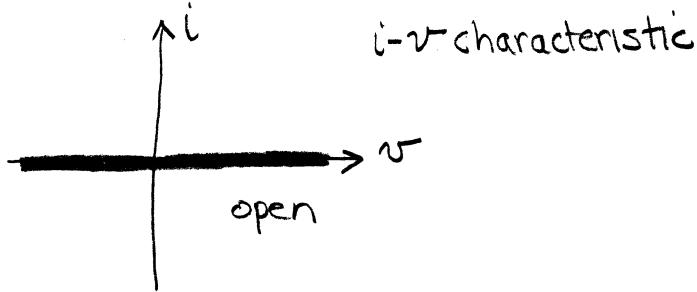
Since $P = vi > 0$ a resistor always absorbs power.

Note: $P = vi = (iR)i = i^2 R$ } we usually use R , rarely G
and $P = vi = v\left(\frac{v}{R}\right) = \frac{v^2}{R}$

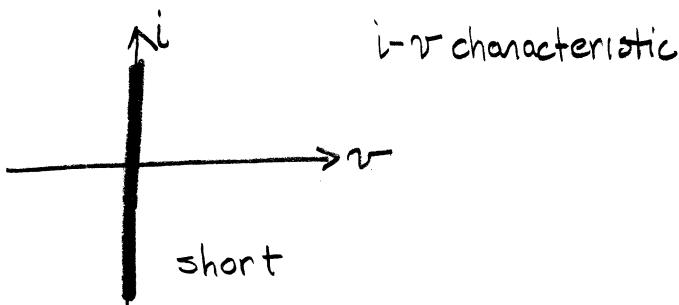
Open and short circuits, switches



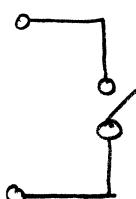
open



short



ideal switch



open position
(OFF)

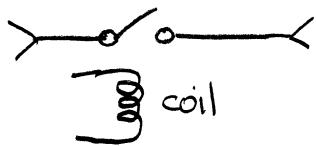


closed position
(ON)

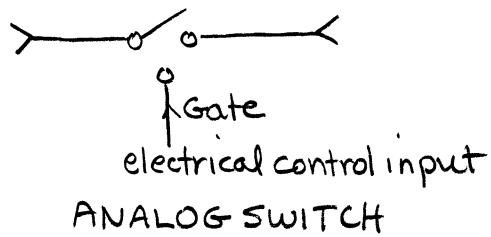
Actual switches have limitations

- maximum current
- maximum voltage
- mechanical actuation (pressure, force)
- operating cycles

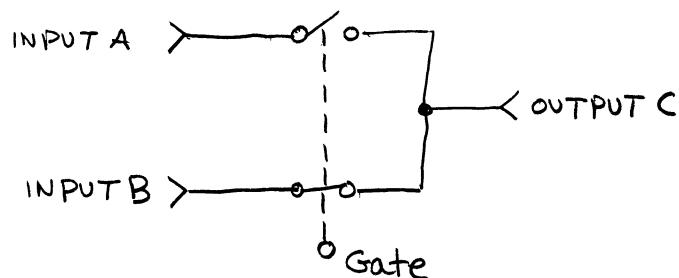
controlled switches
computer controlled switches



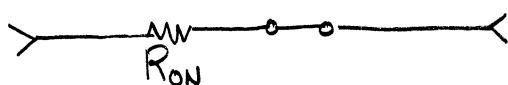
RELAY



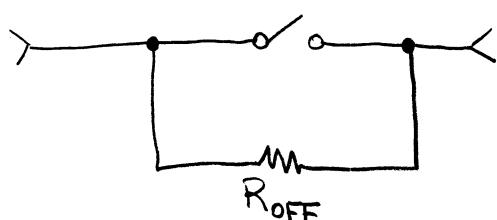
ANALOG SWITCH



MULTIPLEXER (MUX)



switch model with finite ON resistance



switch model with finite OFF resistance