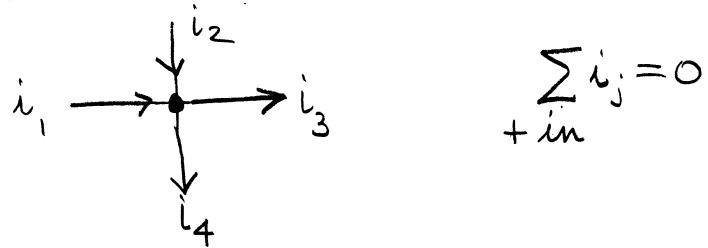
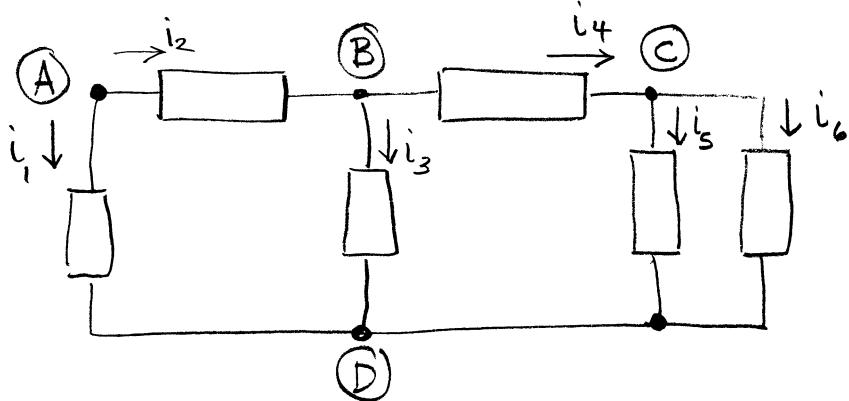


## Kirchoff's Current Law

the algebraic sum of currents entering a node is zero



This is nothing more than conservation of electrons, i.e., what enters the node is equal to what leaves the node.



At node A  $\sum_{+out} i_j = i_1 + i_2 = 0$

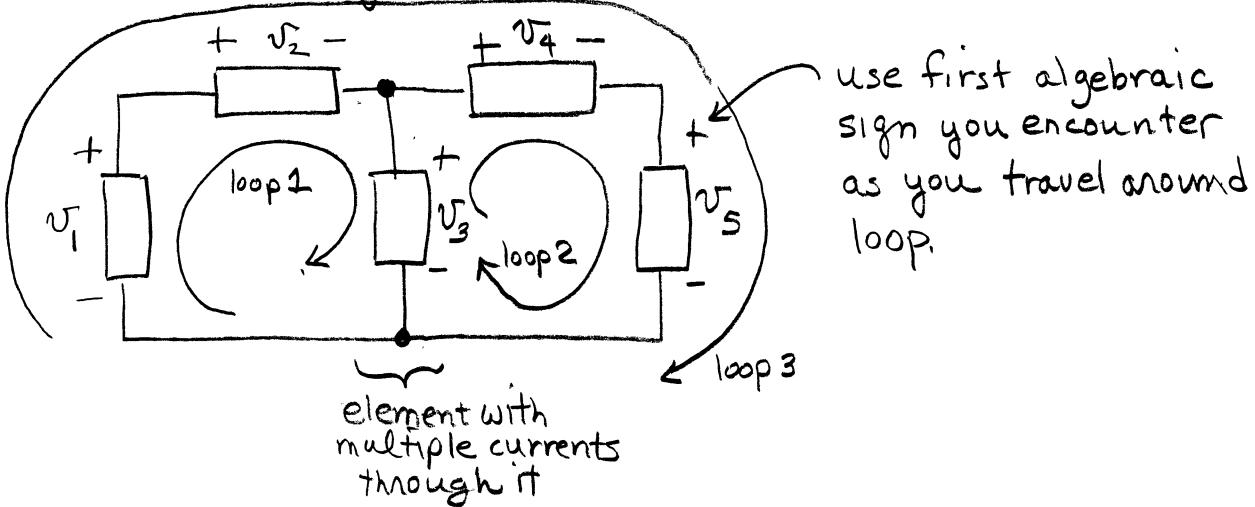
At node B  $\sum_{+out} i_j = -i_2 + i_3 + i_4 = 0$

A circuit with N nodes will have N-1 independent equations from Kirchoff's Current Law.

## Kirchoff's Voltage Law

The algebraic sum of all the voltages around a loop is zero.

IMPORTANT: identify every element by passive sign convention

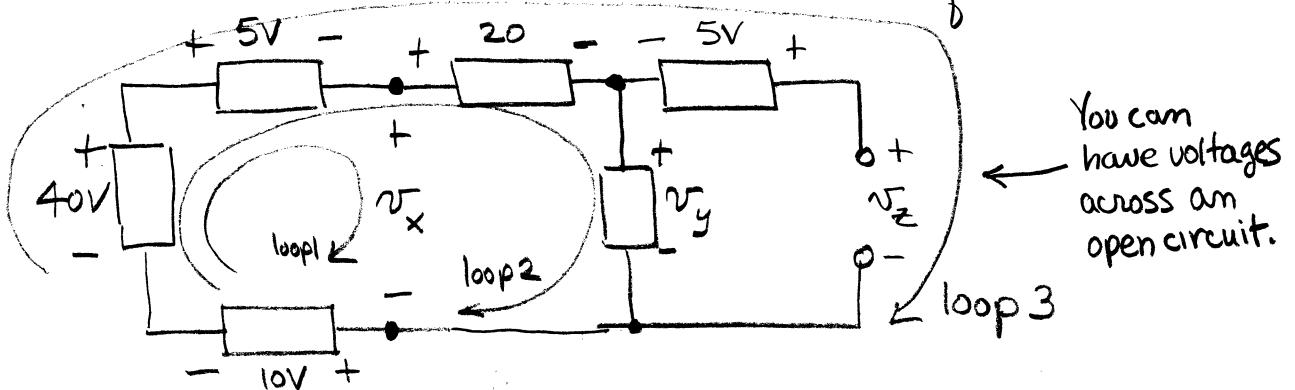


$$\text{loop 1: } -V_1 + V_2 + V_3 = 0$$

$$\text{loop 2: } -V_3 + V_4 + V_5 = 0$$

$$\text{loop 3: } -V_1 + V_2 + V_4 + V_5 = 0$$

If a circuit has  $N$  nodes and  $E$  elements you can write  $E - N + 1$  independent KVL equations.



$$\text{loop 1 gives } V_x: -40 + 5 + V_x + 10 = 0 \quad V_x = +25 \text{ Volts}$$

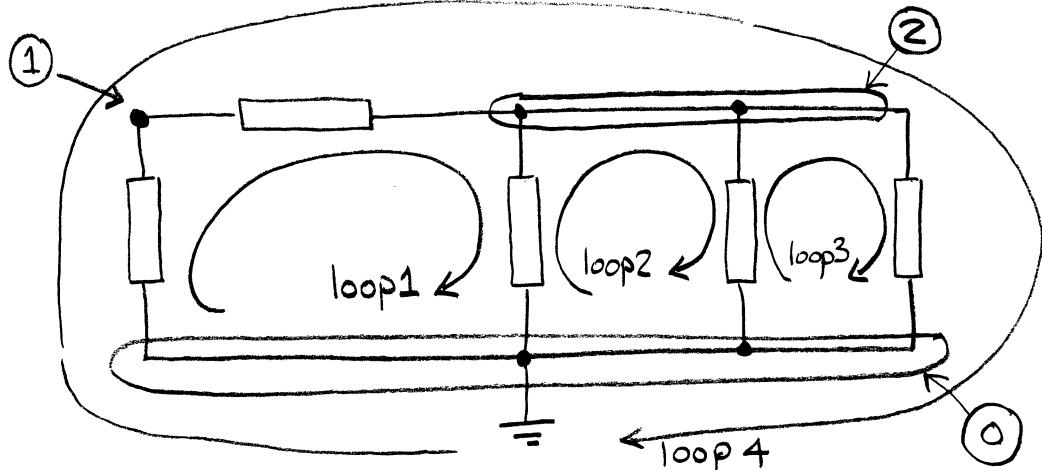
$$\text{loop 2 gives } V_y: -40 + 5 + 20 + V_y + 10 = 0 \quad V_y = +5 \text{ Volts}$$

$$\text{loop 3 gives } V_z: -40 + 5 + 20 - 5 + V_z + 10 = 0 \quad V_z = +10 \text{ Volts}$$

NOTE: You can use many other loops.

## 2-2 Connection constraints

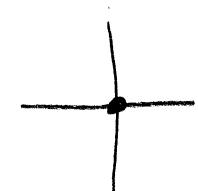
- circuit — interconnection of electrical devices
- node — electrical junction of two or more devices
- loop — closed path formed by tracing through an ordered sequence of nodes without passing through any node more than once



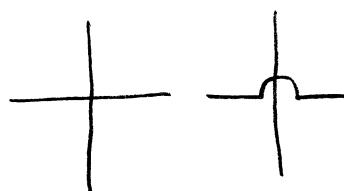
This circuit has three nodes. We always number the ground node  $\emptyset$ .

There are many possible loops. Four are shown.

### symbols



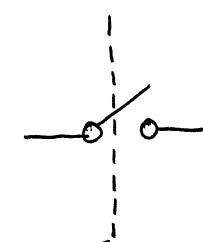
electrical connection



no electrical connection



plug and jack

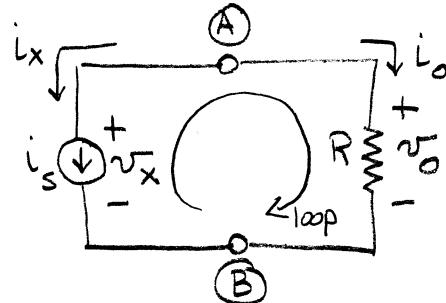


control line

## 2-3 Combined Constraints

Goal is to determine voltages or currents at various places in a circuit.

Initially all circuits will come with reference marks.



We want to know  
( $i_x, v_x$ ) and ( $i_o, v_o$ )  
4 unknowns  $\Rightarrow$  4 eqns

### I. Start with element equations

$$i_s = i_x \quad \text{since current through current source must always be } i_s \quad (1)$$

$$v_o = i_o R \quad \text{Ohm's Law for resistor} \quad (2)$$

### II. Then do connection equations

$$\text{KCL at node A: } \sum_{\text{in}} i = 0 \quad -i_x - i_o = 0 \quad (3)$$

$$\text{KVL around loop } \sum_{\text{loop}} v = 0 \quad -v_x + v_o = 0 \quad (4)$$

This is four equations in four unknowns

$$\begin{aligned} \text{From (1) and (3)} \quad i_o &= -i_x & (3) \\ &i_o = -i_s & \text{substituting (1) into (3)} \end{aligned}$$

Since  $i_s$  is known  $i_x$  and  $i_o$  are now known.

Substituting these results into (2) gives

$$v_o = -i_s R$$

Using (4) now gives  $v_x$  as

$$v_x = v_o = -i_s R$$

The - sign simply means that for the given direction of  $i_s$   $v_o$  is in the opposite polarity to that shown in the drawing