A/D Converter Programming

(1) A-D converter status register (ADCSR) at $11000–READ ONLY; This register shows the status of the A-D converter. Bit 0 is set by the hardware, thus, you CANNOT write to this register. When bit 0 = 1, data has been sampled by the converter and is ready to be processed. Bit 0 resets to 0 immediately after the A-D data register is read. This register is WORD length.

(2) A-D converter data register (ADCDR) at $11002–READ ONLY; This register contains the 16-bit data sampled by the A-D converter. Reading this memory location automatically resets the A-D status register. This register is WORD length.