We report on the analysis of electromechanical coupling effects in suspended doubly-clamped single-layer MoS2 structures, and the designs of suspended-channel field-effect transistors (FETs) and vibrating-channel nanoelectromechanical resonators. In DC gating scenario, signal transduction processes including electrostatic actuation, deflection, straining on bandgap, mobility, carrier density and their intricate cross-interactions, have been analyzed considering strain-enhanced mobility (by up to 4 times), to determine the transfer characteristics. In AC gating scenario and resonant operations (using 100 MHz and 1 GHz devices as relevant targets), we demonstrate that the vibrating-channel MoS2 devices can offer enhanced signals (than the zero-bandgap graphene counterparts), thanks to the resonant straining effects on electron transport of the semiconducting channel. We also show dependence of signal intensity and signal-to-background ratio (SBR) on device geometries and scaling effects, with SBR enhancement by a factor of ~8 for resonance signal, which provide guidelines toward designing future devices with desirable parameters.

Resonant nanoelectromechanical systems (NEMS) have been demonstrating increasing capabilities and prospects for applications in fundamental physics metrologies, sensing and detection of physical quantities near the ultimate limits, and ultralow-power signal processing at radio and microwave frequencies, thanks to their miniaturized sizes and masses, high speeds, and exceptional responsivities and sensitivities. Investigations on the approaches of coupling electrical and mechanical properties in movable nanostructures are an important path toward such prospects; and these also benefit from the emerging materials with attractive new properties, and new techniques of making new nanostructures. Lately, atomic layer two-dimensional (2D) crystals have enabled new types of NEMS resonators with interesting attributes; graphene, in particular, has been extensively studied for 2D NEMS, for its ultralow mass, outstanding elastic properties and superior strain limit (strength).

Only adding to the attractions of graphene as a semimetal, 2D semiconducting crystals, such as atomic layers isolated from transition metal dichalcogenides (TMDCs), also make robust NEMS resonators, creating possibilities for directly coupling mechanical motions into the carrier transport in 2D transistors with sizable bandgap, in ways that may be different than in the graphene counterparts with zero bandgap. Among the TMDCs, molybdenum disulfide (MoS2) is particularly interesting for its thickness-dependent and strain-tunable band structure and mobility, in addition to its ultrahigh strain limit, high elastic modulus and low weight.

MoS2 field-effect transistors (FETs) have been extensively explored for different MoS2 thicknesses, contact materials and device structures, with high $I_{on}/I_{off}$ ratio of more than $10^8$ and mobility ($\mu$) dependent on thickness. Recently, electron mobility of 1020 cm$^2$ (V s)$^{-1}$ for monolayer and 34 000 cm$^2$ (V s)$^{-1}$ for 6-layer MoS2 devices at low temperature have been demonstrated by encapsulating MoS2 in hexagonal boron nitride (h-BN) and using graphene as electrical contact. Room temperature mobility in this type of devices is 40–120 cm$^2$ (V s)$^{-1}$, showing the strong promise of MoS2 as a material for 2D electronics. MoS2 nanomechanical resonators have also been demonstrated, showing frequency up to 83 MHz (ref. 28) and quality (Q) factor up to 710. While optical transduction has been performed for these MoS2 resonators with pre-patterned cavities, understanding the electromechanical coupling and signal transduction, and their dependence on various device parameters in such structures, are highly desired. Although electromechanical coupling effects via gate voltage have been studied in carbon nanotube (CNT) and graphene resonators, the electromechanical coupling that incorporates both the gating effect and straining effect on mobility remains elusive. Further, as a 2D semiconductor with unique electrical and mechanical properties, electromechanical coupling effects in MoS2 could be different from that in 1D CNTs and 2D semimetal graphene. While the study of strain effect on mobility has been attempted in MoS2 transistors, these devices are substrate-supported multilayer...
MoS₂, and the mobility is nearly constant or slightly decreasing with increasing bending. The effect of straining on device mobility for single-layer suspended MoS₂ FETs, and its effect on nanomechanical resonance has not been studied.

In this work, we demonstrate analysis and modeling of electromechanical coupling effects in suspended single-layer MoS₂, and designs of suspended-channel MoS₂ transistors and resonators. In DC scenario of the suspended MoS₂ FETs, we analyze the multi-physics effects on the channel conductance upon electrostatic gating, especially straining effect on enhancing mobility. We solve the electrostatic force and static deflection self-consistently, by first assuming no deflection, and calculate the electrostatic force, then calculate the amount of deflection that the force induces, which in turn increases the electrostatic force; we keep performing the calculation till the solutions of both the electrostatic force and the deflection converge. Moreover, we include the mechanical pull-in effect in a modified configuration (with high-k dielectric) and explore it as a way to improve $I_{on}/I_{off}$ ratio at low operating voltage. From the results obtained in the DC FET modeling, we perform AC and resonance analysis. We demonstrate that by considering the multi-physics coupling in both DC and AC situations, the signal-to-background ratio (SBR) significant enhancement of up to $\sim 8$ times, compared to previous analyses where only gating and capacitance change are considered. We further study the geometric effects on the DC and AC conductance, for varying channel length ($L$) and initial air gap ($g_0$), and we observe interesting scaling and dependency between device geometry and the SBR. The analyses provide important guidelines for future experiments toward efficient electrical readout of suspended single-layer MoS₂ vibrating-channel transistors (VCTs). This platform can also be extended to other 2D semiconductors such as WS₆ and black phosphorus.

When we apply DC or AC gate voltage to the suspended MoS₂ device (Fig. 1a), there will be several effects that modulate the channel conductivity (Fig. 1b). In DC analysis, first, similar to substrate-supported MoS₂ FETs, the gate voltage ($V_G$) modulates the carrier density in the channel by changing the Fermi level of MoS₂. Second, $V_G$ induces deflection in MoS₂ (shown by the blue arrow on the top right of Fig. 1b), which changes the capacitance between MoS₂ and the back gate, and thus changes the carrier density. Note that there is an intricate problem that the electrostatic force induces displacement, which increases the capacitance, and further changes the electrostatic force. To solve this problem and obtain the displacement at certain $V_G$, we develop a Matlab program to calculate the solution of both electrostatic force and displacement self-consistently. Third, the displacement induces strain in the device, which reduces the bandgap of MoS₂, and thus shifts the threshold voltage ($V_T$). The shift in bandgap may also influence the carrier density, by altering the Fermi level; but it has been found theoretically that for monolayer MoS₂, Fermi level does not change much with the application of tensile strain, thus we consider this effect to be small for monolayer devices. Finally, the strain changes the band structure, which changes the effective mass of electrons and reduces phonon scattering, thus enhancing the mobility (shown by the arrows on the right in Fig. 1b). After the carrier density and mobility are determined, we can obtain the channel conductance and drain current ($I_D$) at varying gate voltage, and acquire the transfer characteristics ($I_D$-$V_G$ curve) of the device (Fig. 2). For AC and resonant operations with an added small AC gate voltage, we make use of the DC analysis results, and calculate the near-resonance characteristics while also considering multiple parameters such as the strain effect on mobility, which differs from previous analyses on graphene resonators (where several paths illustrated in Fig. 1 are not considered).

The DC analysis results for a suspended single-layer MoS₂ transistor with MoS₂ length $L = 2 \mu$m, width $w = 1 \mu$m, thickness $t = 0.65$ nm, and initial air gap $g_0 = 290$ nm are shown in Fig. 2. The static deflection ($z_s$) at certain $V_G$ can be obtained by solving the following two equations:

$$-\frac{64}{3L^2}z_s^4 - \frac{8\varepsilon_0}{L}z_s + \frac{F_E L}{E_{Y,2D}} = 0 \quad (1)$$

$$F_E = \frac{1}{2}(g_0 - z_s)\varepsilon_0 E_{Y,2D}^2 \quad (2)$$

where $\varepsilon_0$ is the pre-strain (i.e., initial strain), $F_E$ is the electrostatic force on MoS₂ induced by the back gate, $E_{Y,2D}$ is the 2D
Young’s modulus (180 N m\(^{-1}\)) for monolayer MoS\(_2\), \(\varepsilon'_0\) is vacuum permittivity. At the initial air gap, we can perform a calculation of \(F_E\) using eqn (2), then we obtain the \(z_s\) due to the \(F_E\) by solving eqn (1). With the new air gap \(g = g_0 - z_s\), we calculate \(F_E\) again, and get another \(z_s\). We keep doing this iterative calculation till the fractional difference of both \(z_s\) and \(F_E\) between two calculation steps are smaller than \(10^{-4}\). With the \(z_s\), we can calculate the total strain in the device using \(\varepsilon = \frac{8z_s^2}{3L^2} + \varepsilon'_0\), and then obtain the mobility corresponding to the strain level using the relationship in Fig. 2c inset.\(^{35}\)

With the deflection and mobility, we can calculate the transfer characteristics (\(I_D-V_G\)) of the vibrating-channel transistor using the 2D materials transistor model.\(^{38}\) Using the displacement at certain gate voltage \(V_G\), we determine the characteristic length using \(\lambda = \sqrt{\frac{\varepsilon'_0 L_s I_{th} (g_0 - z_s)}{\varepsilon'_0}}\), where \(\varepsilon'_0 = 4.5\varepsilon'_0\) is the permittivity of MoS\(_2\). We do not explicitly calculate the carrier density, because it later merges with the current calculation. We then calculate the current in two steps. First, with the drain voltage \(V_D\) applied, the source and drain electrostatic potentials (\(\phi_s\) and \(\phi_D\)) are obtained numerically using eqn (6) in ref. 38. Second, the current is obtained using eqn (8) in ref. 38. In the calculation, we assume MoS\(_2\) is doped n-type with work function \(\phi_{MoS_2} = 4.2\) eV, and the flat-band voltage is \(V_{FB} = 0.8\) V for back gate being p-type Si; impurity concentration \(N_{imp} = 10^{13}\) m\(^{-2}\); 2D density of states \(N_{DOS} = 10^{14}\) eV\(^{-1}\) cm\(^{-2}\); temperature is room temperature (300 K). In this study, we focus on the suspended MoS\(_2\) channel, and do not include the contact resistances, but they can be added into the model easily when considering various specific devices with Ohmic contact, in future experiments.

For lower pre-strain levels, we calculate the transfer characteristics assuming different \(\varepsilon'_0\) of 0.06%, 0.17% and 0.28% (Fig. 2a), corresponding to 0.1 N m\(^{-1}\), 0.3 N m\(^{-1}\) and 0.5 N m\(^{-1}\) pre-tension, respectively, because they are reported to be within the range of pre-tension in MoS\(_2\) resonators after pre-patterned microtrenches.\(^{12}\) We find that the transfer curves with 0% and 0.06% pre-strain are quite similar, and the curve with 0.17% pre-strain is only slightly higher, showing that for very low pre-strain, the gate-voltage-induced strain is quite significant compared to the pre-strain (Fig. 2a); specifically, for 0% pre-strain, the total strain at \(V_G = 30\) V is \(\varepsilon = 0.33\%\), and this value changes to 0.34% and 0.37% for 0.06% and 0.17% pre-strain, respectively (Fig. 2b). At 0.28% pre-strain, \(I_D\), total strain, and mobility are all evidently higher than other pre-strain levels (Fig. 2a–c). We also calculate the result without considering the strain effect on enhancing mobility, but include the other effects in Fig. 1b (blue solid lines in Fig. 2a–c). We find that \(I_D\) and \(\mu\) are lower than when we take into account the strain effect on mobility, especially at large \(V_G\). In Fig. 2d–f, we further
examine the effect of higher pre-strain levels on the transfer characteristics of the suspended MoS₂ transistor with the same geometry. We observe that $I_D$ is much higher with higher pre-strain, especially when comparing 1% pre-strain with 0.5% pre-strain (Fig. 2d). An interesting difference from the transfer characteristics with lower pre-strain is that for $\varepsilon_0$ higher than 1%, the $I_D$–$V_G$ curve at “On” state is relatively linear (Fig. 2d), instead of curving up at high $V_G$ for lower pre-strain (Fig. 2a). This is mainly because the gate-voltage-induced strain is much lower than the pre-strain, and the total strain remains almost constant (Fig. 2e), thus mobility and $I_D$ change very little with $V_G$, while mostly change with pre-strain level (Fig. 2f). Further, because at higher pre-strain (>2%), the mobility almost does not change with strain any more (Fig. 2c inset), the transfer characteristics for 2%–10% pre-strain are quite similar.

For a field-effect transistor, $I_D$ increases with decreasing $L$ due to lower resistance. Besides, $I_D$ of an air-gap coupled suspended FET also increases with decreasing $g_0$ due to higher capacitance. However, if we consider the strain effect on mobility, the trend will be different. We investigate $I_D$ dependence on $L$ and $g_0$ under two different conditions: lower pre-strain (0.2%) and higher pre-strain (5%) levels (Fig. 3). At lower pre-strain level, although the maximum $I_D$ is obtained at the shortest $L$ (0.5 μm) and smallest $g_0$ (150 nm), another sharp peak is observed at the longest $L$ (2 μm) and smallest $g_0$ (400 nm) considered in our simulation (Fig. 3a). The reason for this peak is that at longer $L$, larger deflection of the suspended channel is possible at the same $V_G$ (15 V) due to larger electrostatic force and weaker resistance to deflection. Higher deflection of channel results in larger strain, which induces higher mobility and increases $I_D$. On the contrary, for higher pre-strain level, deflection of the suspended channel at the same $V_G$ is much smaller, and the strain will not increase much even for larger $L$. As a result, there is no peak at the longest $L$ for higher pre-strain levels (Fig. 3b). The observation is more obvious in Fig. 3c, which shows the ratio between $I_D$ with and without strain-induced mobility enhancement ($I_D_{Strain}/I_D$), and the peak at the longest $L$ and smallest $g_0$ for lower pre-strain level is observed. This ratio is almost constant for all $L$ and $g_0$ for higher pre-strain level, achieving enhancement of $I_D$ by a

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**Fig. 3** Dependence of DC drain current ($I_D$) on channel length ($L$) and initial air gap ($g_0$), computed for (a) low pre-strain and (b) high pre-strain levels, at $V_G = 15$ V and $V_D = 1$ V. (c) The ratio of drain current with and without strain-induced mobility enhancement ($I_D_{Strain}/I_D$), and the peak at the longest $L$ and smallest $g_0$ for lower pre-strain level is observed. This ratio is almost constant for all $L$ and $g_0$ for higher pre-strain level, achieving enhancement of $I_D$ by a
factor of ∼ 4 after considering the strain effect. The subthreshold swing (SS) decreases with smaller air gap due to larger capacitance (Fig. 3d); and at 30 nm air gap, SS of 74 mV dec⁻¹ is expected.

We have simulated the effect of mechanical pull-in for the suspended single-layer MoS₂ FETs due to the electrostatic force induced by $V_{G}$, using a slightly different geometry, by adding the 5 nm to 10 nm HfO₂ dielectric layer on silicon to prevent the gate from leaking after the mechanical pull-in, as illustrated in Fig. 4a insets. For a suspended single-layer MoS₂ device, as we keep increasing $V_{G}$, the electrostatic force keeps increasing quadratically while the elastic restoring force increases linearly with displacement, and beyond certain gate voltage called pull-in voltage ($V_{PI}$), the electrostatic force is always higher than the elastic force, and the MoS₂ channel is suddenly pulled down. At $V_{PI}$, there exists no solution to eqn (1) and (2); and in the calculation, when the number of iterations reach 10 000, we consider the pull-in effect occurs, and the suspended monolayer MoS₂ is suddenly pulled down. Fig. 4a shows the mechanical pull-in at $V_{PI} = 0.8$ V for a single-layer MoS₂ device with $L = 5$ μm, width $w = 1$ μm, and $g_0 = 60$ nm, at 0.15% pre-strain. With the thin dielectric layer, the device is still functional as a normal FET after the pull-in happens. As a result of the abrupt pull-in, there is a sharp increase of drain current (Fig. 4b). With shorter MoS₂ channel, $V_{PI}$ increases from 0.65 V ($L = 9$ μm) to 3.2 V ($L = 2$ μm) for a device with $g_0 = 70$ nm, and 0.2% pre-strain (Fig. 4b). If we compare a regular, substrate-supported, non-suspended MoS₂ FET with 5 nm HfO₂ (purple solid line in Fig. 4c) to the suspended devices with pull-in effect, we can observe an improvement of the current on-off ratio from 3800 to $3.6 \times 10^4$, if we take $V_{G} = -0.5$ V as off state and $V_{G} = 3$ V as on state (Fig. 4c). $V_{PI}$ increases with higher $g_0$ from 1.2 V at $g_0 = 70$ nm to 2.05 V at $g_0 = 100$ nm (Fig. 4c), and $I_{on}/I_{off}$ ratio also increases with $g_0$ (Fig. 4c inset). For logic circuit operation, it is desirable to define off state at $V_{G} = 0$ V, and this can be achieved by properly designing the geometry to maintain relatively high $I_{on}/I_{off}$ ratio at the same time. Fig. 4d shows that using $L = 3$ μm device with $g_0$ varying from 60 nm to 120 nm, we achieve $I_{on}/I_{off}$ ratio of $1.3 \times 10^4$ with 120 nm air gap, when we take $V_{G} = 0$ V as off state and $V_{G} = 5$ V as on state. The $I_{on}/I_{off}$ ratio also increases with increasing air gap, as shown in Fig. 4d inset.

In AC operation near resonance, the response also increases after we take the strain effect into consideration (Fig. 5a). For
the monolayer MoS2 resonator with $L = 2 \mu m$, $w = 1 \mu m$, and $g_0 = 290$ nm, we calculate the device resonance at 100 MHz assuming the device as a tensioned membrane (Fig. 5b). The resonance frequency is expressed as $f_o = \frac{1}{2\pi} \sqrt{\frac{\kappa E_{2D}}{\rho \alpha \epsilon}}$, where $\rho$ is the 2D mass density and $\alpha$ is adsorbed mass coefficient. If we assume $\alpha = 5$, then 1.4% pre-strain is necessary to achieve 100 MHz resonance frequency, and the strain enhances the mobility and the AC signal we can measure. The channel conductance with both DC and AC gate voltage is

$$G(z) = G(z_0) + \frac{dG}{dV_G} \delta V_G + \frac{dG}{d\delta z} \delta z,$$

where $G(z_0)$ is the conductance under static deflection (which only exists at DC and does not contribute to the signal at resonance frequency), $C_G$ is the capacitance between the MoS2 and the back gate, $\delta V_G$ is the AC gate voltage where $\delta V_G = [\delta V_G] \cos(\omega t)$, $\delta z$ is the AC deflection where $\delta z = [\delta z] \cos(\omega t + \phi)$, with its amplitude $|\delta z| = \sqrt{(\omega^2 - \omega_0^2)^2 + (\omega \omega_0 / Q)^2}$ ($m$ is the mass of the MoS2 resonator), and $\phi = 0$ in ideal case. We can get $dC/dV_G$ from the transfer characteristics under DC static deflection similar to that shown in Fig. 2a and d, obtain $d\mu/d\delta z$ from the $\mu - \epsilon$ relationship in Fig. 2c inset, and find $d\delta z/d\epsilon$ from the deflection relationship $\epsilon = \frac{\delta z^2}{2L^2}$. The second term in eqn (4):

$$\frac{dG}{d\mu} \delta z$$

is a result of the strain effect in enhancing the mobility, which has not been considered before for electromechanically transduced graphene resonator, but its effect is not negligible for suspended monolayer MoS2 devices. Both the strain effect on mobility and the mutual coupling effect between deflection and electrostatic force contribute to the enhancement of the signal strength, as described in eqn (4). As shown in Fig. 5b, we find there is ~4 times enhancement in the peak signal intensity and SBR for the 100 MHz resonance. The signal background is mainly determined by the $dG/dV_G$ term in eqn (3). Here we assume $V_G = 15 \ V$, $|\delta V_G| = 1 \ mV$ and $Q = 1000$. Such $Q$ should be achievable with high quality crystal and by minimizing extrinsic damping effects, or measuring in higher vacuum or at low temperature, because $Q$ of >700 has been achieved with fully-covered circular diaphragms measured in moderate vacuum (6 mTorr), and $Q$ up to $\sim 10^5$ in doubly-clamped graphene resonators has been achieved at 5 K. For 1 GHz resonance in Fig. 5c, we use $L = 1 \mu m$ and $\alpha = 1$ to maintain pre-strain level lower than 10% to avoid metal-insulator transition in MoS2, and obtain that the $\epsilon_0$ needed is 7.2%. With straining effect on mobility, the signal is much higher than without considering this effect. From Fig. 2c inset, the mobility does not increase much when pre-strain is higher than 5%, which means that $d\mu/d\epsilon$ term in eqn (4) is very small, and thus $dG/d\delta z$ is very small. Nonetheless, the current will still be enhanced at pre-strain level $\epsilon_0 = 7.2\%$, because the $dG/dV_G$ term in eqn (4) will be enhanced due to the much higher mobility induced by the strain. Note that the background level is also higher for 1 GHz resonance, so the SBR is not much enhanced, which is different from the 100 MHz device.

We further investigate the effect of varying the device dimension on the SBR for AC gating operation, with comparison against the signal without considering the strain effect (Fig. 6). For 100 MHz case, we vary $L$ from 0.5 \mu m to 2 \mu m, and $g_0$ from 200 nm to 400 nm, and the SBR and SBR enhancement factor (ratio of SBR between considering and not considering the strain effect on mobility) are shown in Fig. 6a and b. SBR as high as ~1435 has been projected, at $L = 1.1 \mu m$ and $g_0 = 200$ nm (Fig. 6a). Smaller $g_0$ makes the electrostatic coupling more efficient, resulting in higher measured current. For length variation, we observe the interesting dependence, and at $L \approx$
1.1 μm we get the best SBR. The effect of $L$ is intricate and it at least includes 4 aspects. Within the length range we consider, we use the effect of larger $L$ as an example. First, with larger $L$, higher pre-strain is needed to attain the same resonance frequency, which translates into higher mobility and higher DC conductance; while the increasing $L$ also results in higher resistance and lower conductance. The combination of these two effects makes $\frac{dG}{dV}$ first decreases, and then increases with $L$. With larger $\frac{dG}{dV}$, the total $G(z)$ at resonance will be higher, and the background is also higher, which would result in lower SBR. Second, $z_s$ increases with larger $L$, resulting in smaller $g_0$, higher capacitance, and especially, higher $\delta z$, which then increases both $G(z)$ and SBR. Third, larger $L$ results in smaller $\delta z$, which decreases both $G(z)$ and SBR. Fourth, $\frac{dG}{d\varepsilon}$ also depends on the strain level, which again depends on $L$. We consider same range of $g_0$ from 200 nm to 400 nm, while different range of $L$ from 0.1 μm to 1 μm, compared to the 100 MHz device, to keep the necessary strain relatively low. The peak SBR is ∼23.8 when $L = 2$ μm and $g_0 = 200$ nm, which is smaller than the peak SBR for the 100 MHz device (Fig. 6c). We obtain the highest SBR enhancement factor of ∼8 at $L \approx 200$ nm and $g_0 = 200$ nm, which is better than the 100 MHz device. We observe different dependence of SBR enhancement factor on $g_0$ than that in the 100 MHz device (Fig. 6d), with higher SBR enhancement factor at smaller $g_0$ for the 1 GHz device.
Recently, piezoelectricity in single-layer MoS$_2$ has been experimentally verified,$^{30,41}$ however, it should not have effect on the generic devices we consider here. First, the piezoelectric effect is only observed when the electrodes are configured along the zigzag direction, so that the bias electric field is along the armchair direction of the single-layer MoS$_2$ crystal.$^{40,41}$ In this work, however, we consider generic MoS$_2$ suspended-channel FETs whose contact electrodes are in arbitrary orientations. Second, even if the drain–source electrodes happen to allow the bias electric field to be along the armchair direction, the occurrence of the piezoelectric effect relies on the Schottky barriers, and the strain-induced charge should asymmetrically modulate the Schottky barriers (the piezotronic effect).$^{40}$ In our calculations, however, we focus on the suspended MoS$_2$ channel rather than the contact electrodes, and we have assumed Ohmic contact without Schottky barriers, and the piezotronic effect will not be evident in this case, because the polarization and charge induced by piezoelectricity will not modulate the Schottky barriers and will be quickly neutralized by free carriers abundant in the electrodes and in MoS$_2$ channel. Third, for experimental realization of the device, Ohmic contact with small contact resistance has been achieved with graphene electrodes.$^{27}$ To rationally design devices to leverage the piezoelectricity effects, crystal orientation and Schottky barriers at the contacts should be carefully engineered, which is beyond the scope of this work.

In summary, we have analyzed the DC static and AC resonance responses of the suspended single-layer MoS$_2$ VCT. Using the self-consistent calculation and taking the strain effect on mobility into consideration, we have first elucidated the effect on DC transfer characteristics, and then extended the calculation to AC resonance analysis, showing that the signal will be higher after considering this effect. We have also examined the effect of geometry (e.g., $L$ and $g_0$) on DC and AC signal that can be measured, which provides important guidelines for future designs and experimental demonstrations of this type of devices.

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